



Docket No.: Z&PINFP08190

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Giuseppe Curello et al.
Applic. No. : 09/904,360 Confirmation No. 1413
Filed : July 12, 2001

Title : Process for Producing a Doped Semiconductor
Substrate

Examiner : Igwe U. Anya
Group Art Unit : 2825
Docket No. : Z&PINFP08190
Customer No. : 24131

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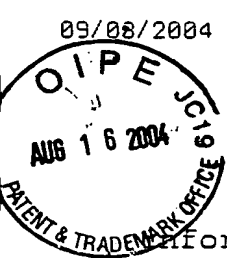
D E C L A R A T I O N under 37 C.F.R. § 1.131

The undersigned Giuseppe Curello and Jürgen Faul hereby declare:

The invention of the above-identified application was
"conceived" and "reduced to practice" at least as early as July
4, 2000.

Enclosed, as corroborating evidence is the Invention Disclosure
(*Erfindungsmeldung*) filled-out and signed by the undersigned.
The Invention Disclosure was executed by the undersigned on July
6, 1999. The Invention Disclosure was given to the Siemens
Patent Department on July 9, 1999.

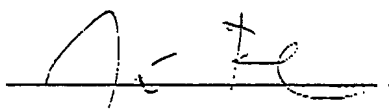
The undersigned declares that all statements made herein of
their own knowledge are true and that all statements made on



Information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. § 1001 and such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Giuseppe Curello

Date: _____, 2004.


Jürgen Faul

Date: June 30th, 2004.



information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. § 1001 and such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Giuseppe Curello

Date: June 25th, 2004.

Jürgen Faul

Date: _____, 2004.